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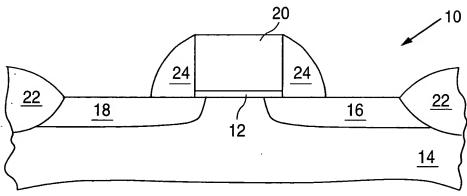


FIG. 1 (PRIOR ART)

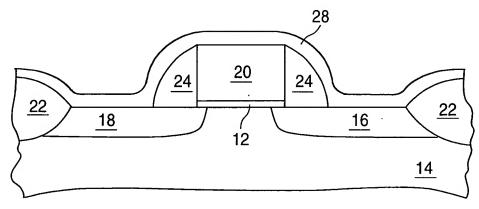


FIG. 2 14 (PRIOR ART)

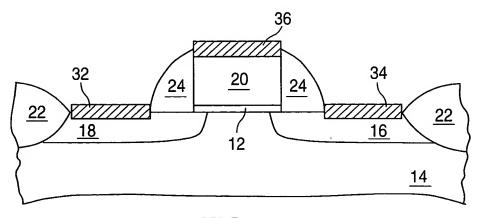


FIG. 3 (PRIOR ART)

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METHOD FOR MANUFACTURING AN
INTEGRATED CIRCUIT STRUCTURE ...
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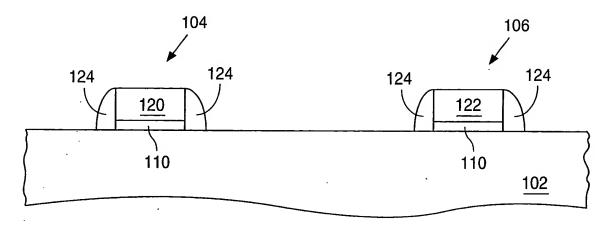


FIG. 4

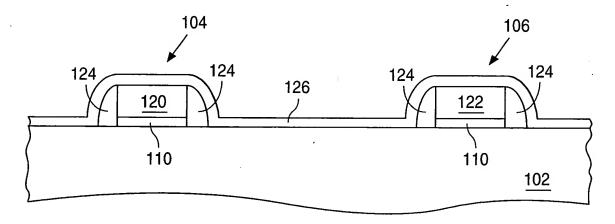


FIG. 5

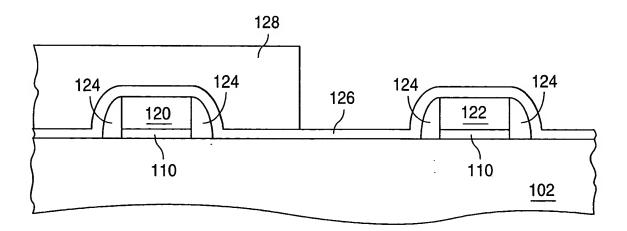


FIG. 6

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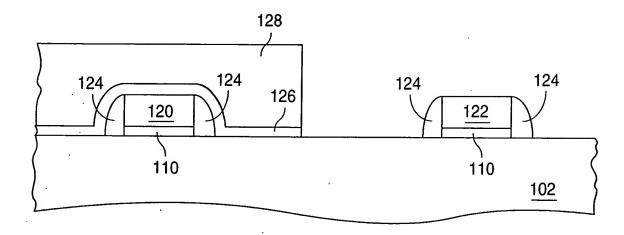


FIG. 7

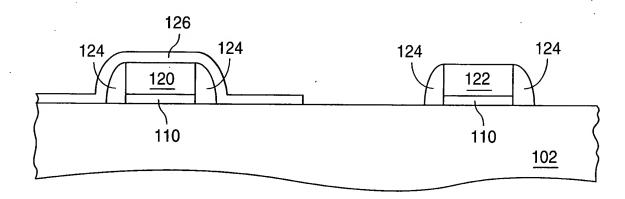


FIG. 8

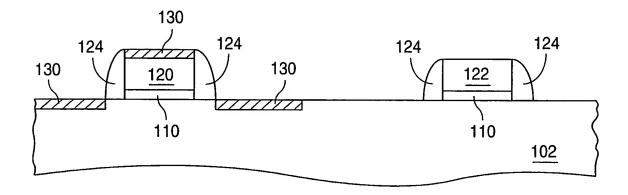


FIG. 9